

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A circuit substrate, comprising:
a substrate;
a plurality of terminals formed on the substrate; and
at least one resistance formed between the terminals adjacent one another;
the plurality of terminals including analog terminals connected to analog signal lines to supply analog signals, and digital terminals connected to digital signal lines to supply digital signals; and

~~one resistance~~ the one resistance having at least one end connected to the analog terminal, and having a resistance value greater than another resistance connected between the digital terminals.

2. (Currently Amended) A circuit substrate, comprising:
a substrate;
a plurality of terminals formed on the substrate; and
at least one resistance formed between the terminals adjacent one another;
the plurality of terminals including first terminals connected to data lines to supply data signals, and second terminals connected to control lines to supply control signals;
and

~~one resistance~~ the one resistance having at least one end connected to the first terminal, and having a resistance value greater than another resistance connected between the second terminals adjacent one another.

3. (Currently Amended) A circuit substrate, comprising:
a substrate;

~~_____ a common electrode line formed on a perimeter of the substrate;~~
~~_____ a plurality of terminals formed on the substrate; and~~
~~_____ at least one resistance formed between the terminals and the common electrode line;~~
~~_____ the plurality of terminals including analog terminals connected to analog signal lines to supply analog signals, and digital terminals connected to digital signal lines to supply digital signals; and~~
~~_____ one resistance connected to the analog terminal having a resistance value greater than another resistance connected to the digital terminal.~~

_____ a plurality of emitting elements formed in a display area, each of the plurality of emitting elements having a first electrode, a second electrode, and an emitting layer between the first electrode and the second electrode, and the first electrode being a common electrode of the plurality of emitting elements;

_____ a common electrode line formed on the perimeter of the display area, the common electrode line connected to the common electrode;

_____ analog signal lines to supply analog signals;

_____ digital signal lines to supply digital signals;

_____ an analog terminal formed on the substrate, the analog terminal being connected to one of the analog signal lines;

_____ a digital terminal formed on the substrate, the digital terminal being connected to one of the digital signal lines;

_____ a first resistor connected between the analog terminal and the common electrode line; and

_____ a second resistor connected between the digital terminal and the common electrode line,

~~the first resistor having a resistance value greater than the second resistor.~~

4. (Currently Amended) A circuit substrate, comprising:

a substrate;

~~a common electrode line formed on a perimeter of the substrate;~~

~~a plurality of terminals formed on the substrate;~~

~~at least one first resistance formed between the terminals adjacent one another;~~

and

~~at least one second resistance formed between the terminals and the common electrode line.~~

~~a plurality of emitting elements formed in a display area, each of the plurality of emitting elements having a first electrode, a second electrode, and an emitting layer between the first electrode and the second electrode, and the first electrode being a common electrode of the plurality of emitting elements;~~

~~a common electrode line formed on the perimeter of the display area, the common electrode line connected to the common electrode;~~

~~a plurality of terminals formed on the substrate, the plurality of terminals including a first terminal and a second terminal;~~

~~a first resistor connected between the first terminal and the second terminal;~~

and

~~a second resistor connected between the common electrode line and the first terminal.~~

5. (Currently Amended) The circuit substrate according to Claim 4, ~~the terminal being connected to both the first resistance and the second resistance; and~~

~~the first resistance-resistor having a resistance value greater than the second resistance-resistor.~~

6. (Currently Amended) The circuit substrate according to Claim 5, the plurality of terminals including analog terminals connected to analog signal lines to supply analog signals, and digital ~~terminal~~terminals connected to digital signal lines to supply digital signals; and

both the first resistance and the second resistance which have at least one end connected to the analog terminal, having resistance values greater than both the first resistance which is connected between the digital terminals, and the second resistance which is connected between the digital terminal and the common electrode line.

7. (Previously Presented) The circuit substrate according to Claim 1, further comprising:

electric power terminals connected to a power source; and
resistances formed between the electric power terminals and adjacent non-electric power terminals formed for purposes other than supplying power.

8. (Previously Presented) The circuit substrate according to Claim 7, the resistance having a resistance value equal to or less than the resistance connected to other non-electric power terminals.

9-11. (Canceled)

12. (Currently Amended) The circuit substrate according to ~~Claim 11~~Claim 1, the ~~resistances~~first resistor and the second resistor being formed of a semiconductor film.

13. (Previously Presented) The circuit substrate according to Claim 1, the resistance including a protection circuit configuration employing PN junction configurations with reverse polarity.

14. (Previously Presented) An electro-optical device, comprising:
the circuit substrate according to Claim 1.

15. (Previously Presented) An electronic apparatus, comprising:

the electro-optical device according to Claim 14.

16. (Withdrawn) A manufacturing method for a circuit substrate that includes a common electrode line on a perimeter of the substrate, and a plurality of terminals on an inner side of the substrate from the common electrode line, the method comprising:

forming at least one first resistance configuration on regions between the terminals adjacent one another;

forming at least one second resistance configuration on regions between the terminals and the common electrode line;

forming the terminals which are electrically connected to a part of at least one of the first resistance configuration and the second resistance configuration; and

forming the common electrode lines which are electrically connected to a part of the second resistance configuration.

17. (Withdrawn) The manufacturing method for a circuit substrate according to Claim 16, the first resistance configuration and the second resistance configuration being formed so that the first resistance configuration has a resistance value greater than the second resistance configuration.

18. (New) A circuit substrate, comprising:

a substrate;

analog signal lines to supply analog signals;

digital signal lines to supply digital signals;

an analog terminal formed on the substrate, the analog terminal being connected to one of the analog signal lines;

digital terminals formed on the substrate, each of the digital terminals being connected to one of the digital signal lines respectively;

a first resistor having at least one end connected to the analog terminal; and

a second resistor connected between the digital terminals,
the first resistor having a resistance value greater than the second resistor.

19. (New) A circuit substrate, comprising:

a substrate;

data lines to supply data signal;

control lines to supply control signals;

a first terminal formed on the substrate, the first terminal being connected to
one of the data lines;

second terminals formed on the substrate, each of the second terminals being
connected to one of the control lines respectively;

a first resistor having at least one end connected to the first terminals; and

a second resistor connected between the second terminals,

the first resistor having a resistance value greater than the second resistor.

20. (New) A circuit substrate comprising:

a substrate;

a plurality of emitting elements formed in a display area, each of the plurality
of emitting elements having a first electrode, a second electrode, and an emitting layer
between the first electrode and the second electrode, and the first electrode being a common
electrode of the plurality of emitting elements;

a common electrode line formed on the perimeter of the display area, the
common electrode line connected to the common electrode;

analog signal lines to supply analog signals;

digital signal lines to supply digital signals;

analog terminals formed on the substrate, each of the analog terminals being
connected to one of the analog signal lines respectively;

digital terminals formed on the substrate, each of the digital terminals being connected to one of the digital signal lines respectively;

a first resistor connected between the common electrode line and one of the analog terminals;

a second resistor connected between the analog terminals;

a third resistor connected between the digital terminals; and

a fourth resistor connected between the common electrode line and one of the digital terminals,

the first resistor having a resistance value greater than both of the third resistor and the fourth resistor, and the second resistor having a resistance value greater than both of the third resistor and the fourth resistor.

21. (New) The circuit substrate according to Claim 19, further comprising:
an electric power terminal connected to a power source; and
a third resistor formed between the electric power terminal and one of the control lines.

22. (New) A circuit substrate according to Claim 21, further comprising:
a fourth resistor having at least one end connected to the electric power terminal, and a third resistor having a resistance value equal to or less than the fourth resistor.

23. (New) The circuit substrate according to Claim 18, the first resistor and the second resistor being formed of a semiconductor film.

24. (New) The circuit substrate according to Claim 18, the first resistor and the second resistor including a protection circuit configuration employing PN junction configurations with reverse polarity.

25. (New) An electro-optical device, comprising:
the circuit substrate according to Claim 18.

26. (New) An electronic apparatus, comprising:
the electro-optical device according to Claim 25.